

Amendments to the Claims

This listing of claims will replace all prior versions, and listings, of the claims in the application.

Listing of Claims:

Claims 1-16 (canceled)

Claim 17 (previously presented): In a computer system having a source computer and a destination computer having a clock that regulates timing of activities at the destination computer, a method comprising the steps of:

providing a logical structure for encapsulating multiple streams of data; wherein:

said streams of data are stored in packets; and

the logical structure holds a field for a maximum packet size and a field for a minimum packet size;

storing clock licenses that dictate advancement of a clock in multiple ones of the packets;

transmitting the logical structure from the source computer to the destination computer; and

for each packet that holds a clock license, advancing the clock at the destination computer as dictated by the clock license in response to receiving the packet at the destination computer.

1 Claim 18 (previously presented): The method of claim 43 wherein each clock
2 license includes a time value to which the clock at the destination computer is to be
3 advanced.

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5 Claim 19 (original): The method of claim 18 wherein each clock license includes
6 an expiration time after which the clock license is invalid.

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8 Claims 20-36 (canceled)

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10 Claim 37 (previously presented): In a computer system, a computer-readable
11 storage medium holding a logical structure that encapsulates components comprising:
12 multiple streams of data wherein the streams of data are stored in packets;
13 clock licenses that each dictate advancement of a clock that regulates rendering of
14 the data in the packets; and
15 a field in the logical structure for holding a value that specifies a maximum bit
16 rate at which the multiple streams of data may be rendered.
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19 Claim 38 (previously presented): The computer-readable storage medium of claim
20 53 wherein each clock license includes a time value to which the clock at the destination
21 computer is to be advanced.

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23 Claim 39 (original): The computer-readable storage medium of claim 38 wherein
24 each clock license includes an expiration time after which the clock license is invalid.
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2 Claims 40-41 (canceled)

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4 Claim 42 (presently presented): A data processing system comprising:

5 a source computer with a storage;

6 a logical structure stored in the storage for encapsulating multiple data streams,
7 data from said data streams being incorporated in packets, wherein:

8 the data stored in the packets are of a new media type;

9 the logical structure stores an identifier for the new media type; and

10 the identifier can be used to determine a renderer to use to render data of

11 the new media type;

12 a clock license being encapsulated into at least one packet for advancing a clock at
13 a destination when processed at the destination.
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16 Claim 43 (previously presented): In a computer system having a source computer
17 and a destination computer having a clock that regulates timing of activities at the
18 destination computer, a method comprising the steps of:

19 providing a logical structure for encapsulating multiple streams of data, said
20 streams of data being stored in packets, by:

21 storing samples of data from multiple data streams in the packets;

22 storing replicas of information in at least some of the packets;
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1 storing error correcting data in the at least some of the packets, wherein
2 the error correcting data identifies an error correcting method for the at least some
3 of the packets;

4 setting a flag in the packets that hold the replicas;

5 storing in the logical structure a field for a maximum packet size and a
6 field for a minimum packet size; and

7 encapsulating the packets into the logical structure, wherein at least some
8 of the packets hold the replicas;

9 storing clock licenses that dictate advancement of a clock in multiple ones of the
10 packets;

11 transmitting the packets of the logical structure on a packet-by-packet basis over a
12 packet switched network from the source computer to the destination computer; and

13 for each packet that holds a clock license, advancing the clock at the destination
14 computer as dictated by the clock license in response to receiving the packet at the
15 destination computer.
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18 Claim 44 (previously presented): The method as defined in Claim 43, wherein the
19 replicas of information hold property information regarding the samples of data.
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21 Claim 45 (previously presented): The method of claim 43 wherein portions of a
22 sample are stored in selected packets and a replica of property information regarding the
23 sample is stored in each packet in which a portion of the sample is stored.
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1 Claim 46 (previously presented): The method of claim 43, further comprising the
2 step of examining one of the replicas of information at the destination computer when one
3 of the packets is lost during the transmitting.
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5 Claim 47 (previously presented): The method of claim 43, further comprising
6 using the error correcting data in the at least some of the packets to correct an error when
7 the transmitted logical structure is received at the destination.
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9 Claim 48 (previously presented): The method of claim 43, wherein:
10 the logical structure includes a header section and a data section; and
11 the error correcting data is stored in multiple packets in the data section.
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13 Claim 49 (previously presented): The method of claim 48, wherein information in
14 the header section of the logical structure indicates what error correcting methodology is
15 used with the error correcting data stored in the multiple packets in the data section.
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17 Claim 50 (previously presented): The method of Claim 48, wherein the header
18 section holds information regarding multiple error correcting methods.
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21 Claim 51 (previously presented): The method of claim 43, wherein the error
22 correcting data identifies one of a plurality of error correcting methods.
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1 Claim 52 (previously presented): The method of claim 43, wherein the error
2 correcting data holds parity bits.

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4 Claim 53 (previously presented): In a computer system, a computer-readable
5 storage medium holding a logical structure that encapsulates components comprising:

6 multiple streams of data wherein the streams of data are stored in packets;

7 a field in the logical structure that holds a value that specifies a maximum bit rate
8 at which the multiple streams of data may be rendered; and

9 clock licenses that each dictate advancement of a clock that regulates rendering of
10 the data in the packets, wherein:

11 the streams of data stored in the packets are samples of data from multiple
12 data streams in packets for transmission on a packet-by-packet basis over a packet
13 switched network;

14 replicas of information are stored in at least some of the packets;

15 error correcting data is stored in the at least some of the packets;

16 the error correcting data identifies an error correcting method for the at
17 least some of the packets; and

18 a flag is stored in each said packet that holds the replicas.
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21 Claim 54 (previously presented): The computer-readable storage medium of claim
22 53 wherein portions of a sample are stored in selected packets and a replica of property
23 information regarding the sample is stored in each packet in which a portion of the
24 sample is stored.
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1 Claim 55 (previously presented): The computer-readable storage medium as
2 defined in claim 53, wherein:

3 the logical structure includes a header section and a data section, and

4 the error correcting data is stored in multiple packets in the data section.

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6 Claim 56 (previously presented): The computer-readable storage medium as
7 defined in claim 55, wherein the information in the header section of the logical structure
8 indicates what error correcting methodology is used with the error correcting data stored
9 in the multiple packets in the data section.
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11 Claim 57 (previously presented): The computer-readable storage medium as
12 defined in claim 56, wherein the header section holds information regarding multiple
13 error correcting methods.
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16 Claim 58 (previously presented): The computer-readable storage medium as
17 defined in claim 53, wherein the error correcting data identifies a plurality of error
18 correcting methods.
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20 Claim 59 (previously presented): The computer-readable storage medium as
21 defined in claim 53, wherein the error correcting data holds parity bits.
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23 Claim 60 (previously presented): A data processing system comprising:
24 a source computer with a storage;
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1 a logical structure stored in the storage for encapsulating multiple data streams,
2 data from said data streams being of a new media type and incorporated in packets,
3 wherein the logical structure includes an identifier of the new media type from which a
4 renderer can be determined to render the data of the new media type; and
5 a clock license being encapsulated into at least one packet for advancing a clock at
6 a destination when processed at the destination, wherein:
7 the streams of data stored in the packets are samples of data from multiple
8 data streams in the packets for transmission on a packet-by-packet basis over a
9 packet switched network;
10 replicas of information are stored in at least some of the packets;
11 error correcting data is stored in the at least some of the packets;
12 the error correcting data identifies an error correcting method for the at
13 least some of the packets; and
14 a flag is stored in each said packet that holds the replicas.

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17 Claim 61 (previously presented): A data processing system comprising:
18 a source computer with a storage;
19 a logical structure stored in the storage for encapsulating multiple data streams,
20 wherein:
21 the data from said data streams is incorporated in packets; and
22 the multiple streams of data in the logical structure are Active Stream
23 Format (ASF) data streams; and
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1 a clock license being encapsulated into at least one packet for advancing a clock at
2 a destination when processed at the destination, wherein portions of a sample are stored
3 in selected packets and a replica of property information regarding the sample is stored in
4 each packet in which a portion of the sample is stored.

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6 Claim 62 (previously presented): The data processing system as defined in claim
7 60, wherein:

8 the logical structure includes a header section and a data section, and
9 the error correcting data is stored in multiple packets in the data section.
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11 Claim 63 (previously presented): The data processing system as defined in
12 claim 62, wherein information in the header section of the logical structure indicates what
13 error correcting methodology is used with the error correcting data stored in the multiple
14 packets in the data section.
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17 Claim 64 (previously presented): The data processing system as defined in claim
18 63, wherein the header section holds information regarding multiple error correcting
19 methods.
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21 Claim 65 (previously presented): The data processing system as defined in claim
22 60, wherein the error correcting data identifies a plurality of error correcting methods.
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1 Claim 66 (previously presented): The data processing system as defined in claim
2 60, wherein the error correcting data holds parity bits.

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4 Claim 67 (previously presented): The method as defined in Claim 17, wherein the
5 multiple streams of data in the logical structure are Active Stream Format (ASF) data
6 streams.

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8 Claim 68 (previously presented): The method as defined in Claim 17, wherein the
9 logical structure holds a field for a maximum bit rate at which the multiple streams of
10 data may be rendered at the destination.

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12 Claim 69 (presently presented): The method as defined in Claim 17, wherein:
13 the logical structure holds a field for a new media type; and
14 the method further comprises:
15 accessing the field that identifies the new media type, upon receipt of the logical
16 structure by the destination computer to determine a renderer to use to render data of the
17 media type.
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20 Claim 70 (previously presented): The computer-readable storage medium as
21 defined in Claim 37, wherein the logical structure that encapsulates components further
22 comprises a maximum packet size and a minimum packet size
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1 Claim 71 (previously presented): The computer-readable storage medium as
2 defined in Claim 37, wherein the multiple streams of data in the logical structure are
3 Active Stream Format (ASF) data streams.
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5 Claim 72 (previously presented): The computer-readable storage medium as
6 defined in Claim 37, wherein:

7 the streams of data stored in packets are of a new media type; and

8 the new media type can be used to determine a renderer to use to render data of
9 the new media type.
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11 Claim 73 (previously presented): The data processing system as defined in Claim
12 42, wherein the logical structure holds a field for a maximum packet size and a field for a
13 minimum packet size.
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16 Claim 74 (previously presented): The data processing system as defined in Claim
17 42, wherein the multiple streams of data in the logical structure are Active Stream Format
18 (ASF) data streams.
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20 Claim 75 (previously presented): The data processing system as defined in Claim
21 42, wherein the logical structure holds a value that specifies a maximum bit rate at which
22 the multiple streams of data may be rendered.
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1 Claim 76 (previously presented): The method as defined in Claim 43, wherein the
2 multiple streams of data in the logical structure are Active Stream Format (ASF) data
3 streams.

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5 Claim 77 (previously presented): The method as defined in Claim 43, further
6 comprising including a field in the logical structure for holding a value that specifies a
7 maximum bit rate at which the multiple streams of data may be rendered at the
8 destination computer.

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10 Claim 78 (presently presented): The method as defined in Claim 43, wherein
11 further comprising:

12 dynamically defining a new media type for a stream format of the multiple
13 streams of data;

14 storing in the logical structure an identifier of the new media type that adopts the
15 stream format; and

16 accessing, at the destination computer, the identifier of the new media type to
17 identify a renderer to use to render data of the new media type.
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20 Claim 79 (previously presented): The computer-readable storage medium as
21 defined in Claim 53, wherein the logical structure that encapsulates components further
22 comprises a field for a maximum packet size and a field for a minimum packet size.
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1 Claim 80 (previously presented): The computer-readable storage medium as
2 defined in Claim 53, wherein the multiple streams of data in the logical structure are
3 Active Stream Format (ASF) data streams.
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5 Claim 81 (presently presented): The computer-readable storage medium as
6 defined in Claim 53, wherein:
7 the logical structure that encapsulates components further comprises a field for a
8 new media type for the streams of data stored in the packets; and
9 the new media type identifies a renderer to use to render data of the new media
10 type.
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12 Claim 82 (previously presented): The data processing system as defined in Claim
13 60, wherein the logical structure includes a field for a maximum packet size and a field
14 for a minimum packet size.
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17 Claim 83 (previously presented): The data processing system as defined in Claim
18 60, wherein the multiple streams of data in the logical structure are Active Stream Format
19 (ASF) data streams.
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21 Claim 84 (previously presented): The data processing system as defined in Claim
22 60, wherein the logical structure includes a field for holding a value that specifies a
23 maximum bit rate at which the multiple streams of data may be rendered.
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1 Claim 85 (previously presented): The data processing system as defined in Claim
2 61, wherein the logical structure includes a field for a maximum packet size and a field
3 for a minimum packet size.
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5 Claim 86 (previously presented): The data processing system as defined in Claim
6 61, wherein the logical structure includes a field for holding a value that specifies a
7 maximum bit rate at which the multiple streams of data may be rendered.
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9 Claim 87 (previously presented): The data processing system as defined in Claim
10 61, wherein the logical structure includes a field for an identifier of a new media type for
11 the data from said data streams incorporated in the packets and from which a renderer can
12 be determined to render the data of the new media type.
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14 Claim 88 (presently presented): The method as defined in Claim 43, wherein:
15 the multiple streams of data in the logical structure are Active Stream Format
16 (ASF) data streams; and
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18 the method further comprising:

19 including a field in the logical structure for holding a value that specifies a
20 maximum bit rate at which the multiple streams of data may be rendered at the
21 destination computer;

22 dynamically defining a new media type for a stream format of the multiple
23 streams of data;
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1 storing in the logical structure an identifier of the new media type that
2 adopts the stream format; and

3 accessing, at the destination computer, the identifier of the new media type
4 to identify a renderer to use to render data of the new media type.

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6 Claim 89 (presently presented): The computer-readable storage medium as
7 defined in Claim 53, wherein:

8 the logical structure that encapsulates components further comprises:

9 a field for a maximum packet size and a field for a minimum packet size;

10 and

11 a field for a new media type for the streams of data stored in the packets

12 the multiple streams of data in the logical structure are Active Stream Format
13 (ASF) data streams; and

14 the new media type identifies a renderer to use to render data of the new media
15 type.
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